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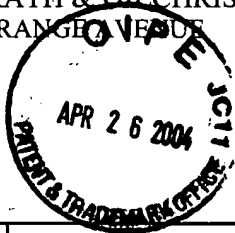
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| | |
|---------------------|--------------------|
| JUSTIN GOREE | (Depositor's name) |
| <i>Justin Goree</i> | (Signature) |
| APRIL 22, 2004 | (Date) |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/042,520 | 01/09/2002 | Philippe Coronel | 00GR32554365 | 6898 |

TITLE OF INVENTION: DRAM MEMORY INTEGRATION METHOD

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 05/17/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|-------------------|----------|----------------|
| KESHAVAN, BELUR V | 2825 | 438-396000 |

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1. LISA K. JORGENSEN;
2. ALLEN, DYER, DOPPELT, MILBRATH & GILCHRIST, P.A.

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

STMICROELECTRONICS SA

MONTROUGE, FRANCE

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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